



HOLY MARY INSTITUTE OF TECHNOLOGY & SCIENCE

(UGC AUTONOMOUS)

(Approved by AICTE, New Delhi, and Permanent Affiliation to JNTUH, Accredited by NAAC 'A' grade)
 Bogaram (V), Keesara (M), Medchal (Dist), Hyderabad, Telangana State

EXAMINATION BRANCH

I-M.Tech I-SEMESTER-HITS R21 REGULATION SEMESTER END EVALUATION EXAMINATIONS MAY-2022

TIME TABLE

TIME → FN: 10.00AM TO 1.00 PM

BRANCH	DATE, DAY AND SESSION				
	02-05-2022 FN MONDAY	05-05-2022 FN THURSDAY	07-05-2022 FN SATURDAY	09-05-2022 FN MONDAY	11-05-2022 FN WEDNESDAY
HIGHWAY ENGINEERING	Highway Policy & Planning	Highway Material Characterization	Evaluation of Strengthening of Pavements	Road Safety Engineering	Traffic Engineering
ELECTRICAL POWER SYSTEMS	Power System Analysis	Economic Operation of Power Systems	Renewable Energy Systems	HVDC Transmission	Reactive Power Compensation and Management
POWER ELECTRONICS	Power Electronic Converters	Machine Modeling and Analysis	Power Electronics for Renewable Energy Systems	Smart Grid Technologies	Power Semiconductor Devices and Modeling
CAD/CAM	Advanced CAD	Computer Aided Manufacturing	Performance Modeling and Analysis of Manufacturing	Additive Manufacturing Technologies	Automation in Manufacturing

DATE: 26-03-2022

[Signature]
 CoE Controller of Examinations & Sciences

Holy Mary Institute of Technology & Science
 (UGC-Autonomous)

Bogaram (V), Keesara (M), Medchal (Dist)-501 301, T.S.

[Signature]
 Principal/Chief Controller of Examinations & Sciences

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EMBEDDED SYSTEMS	System Design with Embedded Linux	Microcontrollers & Programmable Digital Signal Processors	Hardware Software Co-Design	Artificial Intelligence & Machine Learning	Advanced Computer Architecture
VLSI DESIGN	Digital Design and Verification	Microcontrollers and Programmable Digital Signal Processors	Low Power VLSI Design	Full Custom IC Design	IOT and Its Application
COMPUTER SCIENCE & ENGINEERING	Advanced Data Structures	Mathematical Foundations of Computer Science	Machine Learning	Advanced Artificial Intelligence	Cloud Computing

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